

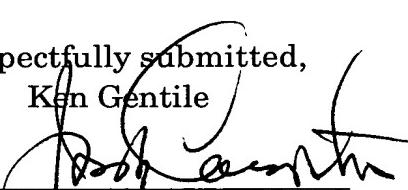
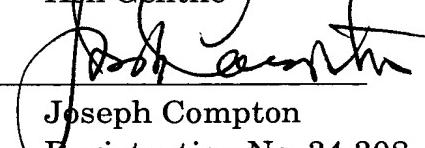
REMARKS

Claims 1-10, 12-15, 17-20, 22, 23, 25 and 26 have been canceled, claims 11, 16, 21 and 24 have been amended and claims 27, 28 and 29 have been added. Claims 11, 16, 21, 24, and 27-29 remain in the application.

This preliminary amendment forms the submission required in 37 CFR §1.114 for a Request for Continued Examination as its amended and added claims patentably distinguish over the cited art. In particular, the art that was cited in the Notice of References Cited and the Information Disclosure Statement by Applicant which were both included in the Office Action of September 15, 2004.

Section 2131 of the Manual of Patent Examining Procedure (MPEP) states, "a claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single prior art reference". Applicant submits that each of the cited art fails to teach the structure recited in amended independent claims 11 and 21 and added independent claim 27 and, accordingly, these claims are not anticipated by any of the cited art.

As selected examples of this failure, each reference of the cited art fails to teach the combination of divider, buffer register and latches recited in claims 11 and 21 that provides data substreams in response to a reduced clock signal which is provided by the divider. Each reference of the cited art further fails to teach the combination of divider, buffer store and latches recited in claim 27 that also provides data substreams. In addition, each reference of the cited art fails to teach the use of buffer registers, digital multipliers and digital summers recited in claims 11 and 21 nor the use of a data processor that is recited in claim 27.

Respectfully submitted,
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